

INFORMATION ONLY – DOES NOT COMMUNICATE
A MODIFICATION OR SAFETY CONDITION

16719A-01

S E R V I C E N O T E

Supersedes:
NONE

Agilent 16717A, 16718A, & 16719A Logic Analyzer Modules

Serial Numbers: US00000000/US99999999, MY00000000/MY99999999

Correction to the Agilent 16717A, 16718A, & 16719A series logic analyzer module field calibration procedure in Service Guide P/N 16715-97003

Parts Required:

| P/N | Description | Qty. |
|------|-------------|------|
| NONE | | |

Situation:

In the 333 MHz State Mode test on page 109 of the service guide, the operator is directed to assign a double edge to the logic analyzer J-clock. As it is configured, the pulse generator will not deliver the correct clock signal for double edge clocking. Consequently the operator will observe test data not consistent with the service guide calibration procedure.

Solution/Action:

Configure the pulse generator for divide-by-1 on channel 2. The table under Set up the equipment step 2 (page 103) shall read:

| Timebase | Channel 2 |
|------------------|------------------------------------|
| Period: 6.006 ns | Mode: SQUARE Divide: SQUARE ÷ 1 |

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:

INFORMATION ONLY

AUTHOR: DF PRODUCT LINE: 1A

ADDITIONAL INFORMATION: 16717A, 16718A, & m16719A Service Guide 16715-97003

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